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01/25/02

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10057205	FILING DATE 01/25/2002	CLASS 438	EXAMINER 2329 GEYER
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**APPLICANTS: Quinlan Sign Sales Inc.

**CONTINUING DATA VERIFIED:

NONE SBF

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** FOREIGN APPLICATIONS VERIFIED:

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SBG

DO PUBLISH NOT PUBLISH ☐

RESCIND ☐

For a priority claim:

☒ yes ☐ no

For a priority claim:

☒ yes ☐ no

Verified and Acknowledged by Examiner's initials

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ATTORNEY DOCKET NO

500001.02

Abstract: Semiconductor package assembly and method for electrically isolating modules

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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